

SCHOOL OF ENGINEERING

DEPARTMENT OF

ELECTRONICS AND COMMUNICATION ENGINEERING

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NECTOR

NNRG EC TECHNOCRATS ORNATE REPORT

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NECTOR-ECE





Nalla Narasimha
Reddy Group of
Institutions

NNRG
Integrated Campus

Engineering • Pharmacy • MBA



**DEPARTMENT
OF
ELECTRONICS AND COMMUNICATION ENGINEERING**

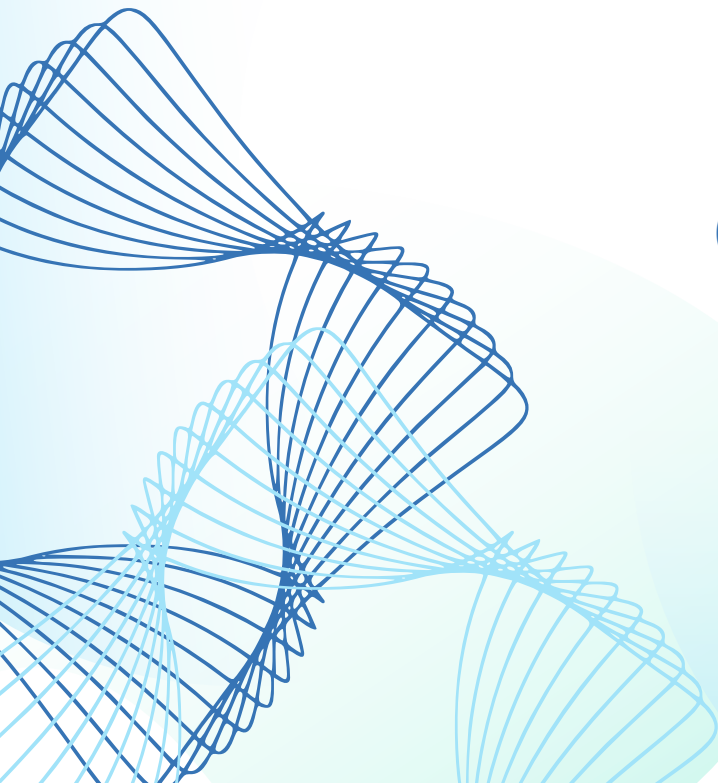


NNRG EC TECHNOCRATS ORNATE REPORT

NECTOR

**2025-2026
(MARCH-APRIL)**

www.nnrg.edu.in
NECTOR-ECE



SCHOOL OF ENGINEERING
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

INSTITUTE'S VISION AND MISSION

VISION:

- To be a premier institution ensuring globally competent and ethically strong professionals.

MISSION:

- To provide higher education by refining the traditional methods of teaching to make globally competent professionals.
- To impart quality education by providing the state-of-the-art infrastructure and innovative research facilities.
- To practice and promote high standards of professional ethics, transparency and accountability.

DEPARTMENT'S VISION AND MISSION

VISION:

- To produce creative Electronics and Communication Engineering graduates with cutting edge technology and Research to meet Industry and societal needs.

MISSION:

- To provide innovative learning environment to enable the students to face the challenges.
- To provide value-based education by promoting activities addressing societal needs.
- To enable graduates to develop the skills to solve complex problems in multidisciplinary activities.

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1.SALIENT FEATURES

ABOUT ECE DEPARTMENT

Electronics is the branch of study that has revolutionized the life style of humanity. It is a pace setter and a prime mover behind the transition to a technological society. The field of Electronics and Communication Engineering encompasses all areas of human life. Radio, television, telephones, computers, automobiles, office machinery and house-hold appliances, lifesaving medical equipment and space vehicles represent a mere sample in the wide spectrum of application of Electronics.

In the age of satellite transmitted television and transcontinental computer network, the challenge and opportunities in this profession continue to mushroom. Electronics and Communication Engineering graduates have unlimited opportunities in the field of terrestrial and extra-terrestrial communication systems like telephones, cellular phones, television, optical fiber communication, consumer and entertainment devices. Highly rewarding and greatly satisfying opportunities await the Electronics and Communication Engineers in the field of satellite space programs, embedded technologies etc.

The Department of Electronics and Communication was started in the year 2009. It is a full- fledged academic department for emerging ECE engineers. B.Tech. (ECE) program was duly approved by the AICTE and affiliated to JNTU, Hyderabad. An intake of 60 was sanctioned initially and then it is raised to 180 from the year 2019. PG Program (M.Tech.) with specialization in “Embedded Systems &VLSI” is approved by AICTE for the academic year 2013-14 with an approved intake of 18. For the academic year 2024-25, there are 11 students enrolled in the booming M.Tech Program successfully.

There are 33 Faculty members in the department which includes 8 doctorates from various reputed institutions, and 12 faculty members pursuing their Ph.D. The department is equipped with 8 spacious and well-developed laboratories. Seminars, Workshops and Technical Symposia are regularly conducted in the department to keep faculty and students updated with latest developments in various fields. The department is committed to provide opportunity to embark on a rewarding and exciting learning journey in preparation for a future career.

The Electronics & Communication Engineering (ECE) Department has built a reputation for excellence in teaching and service. Electronic engineers are changing the world to a comfortable global home. The information and technology revolution has been built on the advances of Electronics.

ECE is making exhilarating progress in areas ranging from mi microelectronics, mobile communications to VLSI Design Automation. The students are inspired by the expertise and knowledge of our faculty, integrating practical with challenging and interesting course-work

The software's and major Equipment that are available in the department are:

- Cadence EDA Tool
- MATLAB-2014b and Tool Boxes (Simulink, Image Processing, Communication, Signal Processing and Digital Signal Processing System)
- Spectrum Analyzer and Signal Generator
- OFDM trainer kit
- Digital Signal Processors Kits (TMS320C6713)
- MPMC COTEX M3 Kit
- Advanced RISC Machine Processor Kits (LPC2148)
- Xilinx SPARTAN 3E FPGA Boards
- Artix-7 FPGA kit, Zynq-7000 SoC kit
- Xilinx Vivado tool


2.DEPARTMENT EVENTS

2.1 ELITE GATHERING


2.1.1 III ECE B.TECH

 **Event:** Elite Gathering (31 March 2026)

 **Venue:** NNRG Auditorium (ECE Dept.)

 **Participants:** III Year ECE (Sections A, B, C – 107 students)

 **Theme:** “**Knowledge with Purpose: Social Responsibility of Young Generation**”

 Focus on using education for real-world impact & social good

 **Activities:**

Student speeches & presentations

Discussions on ethics, innovation & leadership

Participation from multiple student speakers

 **Key Outcomes:**


Awareness of social responsibility

Motivation to apply knowledge in real life

Development of leadership & problem-solving skills

 **Overall:**

A meaningful program encouraging students to become responsible and impactful individuals

 This is just a glimpse — the real story, insights, and impact are waiting for you. Click here to read more:

https://nnrg.edu.in/PDF/events/ECE/2025_26_EG_20260106.pdf


2.1.2 II ECE B.TECH

 **Event:** Elite Gathering (10 March 2026)

 **Venue:** NNRG Auditorium

 **Participants:** II Year ECE students

 **Theme: “Women in Leadership: Shaping the Future”**

 Focus on empowering women & leadership roles

 **Activities:**

Student speeches & presentations

Sharing ideas on women empowerment & innovation

Active participation from multiple speakers

 **Key Outcomes:**


Inspired confidence & leadership mindset

Awareness about women’s role in society

Encouraged skills, mentorship & collaboration

 **Overall:**


A motivating program promoting women empowerment and future leadership


 This is just a glimpse — the real story, insights, and impact are waiting for you. Click here to read more:

https://nnrg.edu.in/PDF/events/ECE/2025_26_EG_20260106.pdf

2.2 CLUB AWARENESS PROGRAM

"ACADEMIC CLUB ACTIVITY REPORT"

 **Event:** DAA Hour (Academic Club Activity)

 **Date:** 03 Jan 2026

 **Venue:** ECE Seminar Hall

 **Participants:** ~145 II Year ECE students

Objective:

Create awareness about club domains like VLSI, Communications, IoT & Signal Processing

Sessions Covered:


- o ♦ VLSI – Basics of chip design & semiconductor careers
- o ♦ Communications & Networking – Data transmission & modern networks
- o ♦ Embedded Systems & IoT – Smart systems & real-time applications
- o ♦ Signal & Image Processing – Analysis of signals, images & AI applications

Key Takeaway:

Gained knowledge on core ECE fields & career opportunities

Overall:

Informative session giving technical exposure & domain awareness

 Click here to explore the full story behind this achievement:

<https://nnerg.edu.in/PDF/events/ECE/Club%20Awareness%20Event.pdf>

2.3 WORKSHOP RELATED TO ML&DL

Workshop by IBM Skill Build in collaboration with Magic Bus Indian Foundation

- 📌 3-day workshop on AI, ML & Deep Learning (Feb 10–12, 2026)
- 👤 Conducted by IBM Skill Build & Magic Bus India
- 👨‍🎓 ~140 ECE students participated
- 📖 Covered basics → algorithms → real-time applications
- 🔧 Included interactive & hands-on sessions
- 🧠 Improved technical, analytical & problem-solving skills
- 🚀 Created awareness about AI careers & industry trends
- ✅ Overall: Highly beneficial & skill-enhancing program

The banner features the NNRG logo on the left and accreditation logos (A+, NAAC, NBA, and All India Council for Technical Education) on the right. The central text reads: **NALLA NARASIMHA REDDY** Education Society's Group of Institutions–Integrated Campus (UGC AUTONOMOUS INSTITUTION) **School of Engineering** In association with IIC & IIIC **Department of Electronics and Communication Engineering** Organizing **A Three Day Workshop on Machine Learning and Deep Learning** by **Mr. VISHAL MISHRA** Training Manager, Magic Bus India. The bottom section contains the date **Date: 10/02/2026 to 12/02/2026** and the venue **Venue: ECE Seminar Hall**. There are also decorative icons of a brain and a circuit board.

🔍 Dive deeper into the insights — click here to read more:
https://nnrg.edu.in/PDF/events/ECE/SDP1_Report.pdf

3. INSTITUTIONAL EVENTS

3.1 IIIC EVENT-THE FUTURE OF SEMICONDUCTOR INDUSTRY IN INDIA

NALLA NARASIMHA REDDY
Education Society's Group of Institutions-Integrated Campus
UGC AUTONOMOUS INSTITUTION
www.nnrg.edu.in

Industry Institute Interaction Cell (IIIC)
Organizes
Industry Expert Talk
on
THE FUTURE OF SEMICONDUCTOR INDUSTRY IN INDIA

Ms. Pragati Thakur
Senior HR Manager,
Cadence, Hyderabad

Date: 03rd January 2026 Time: 11:00 AM Venue: Conference Hall, Ground Floor

Program Flexi

Overview of Program:

The Industry Institute Interaction Cell (IIIC) of Nalla Narasimha Reddy Education Society's Group of Institutions (NNRG), Hyderabad, organized an Industry Expert Talk on "The Future of Semiconductor Industry in India" on 03rd January 2026 at 11:00 AM in the Conference Hall, Ground Floor, NNRG Campus. The session was delivered by Ms. Pragati Thakur, Senior HR Manager, Cadence, Hyderabad.

Purpose of Program:

The program was mainly aimed to provide students with industry exposure, trends and technologies currently running and to create awareness about the semiconductor ecosystem in India, and guide them on career opportunities, skill requirements, and future trends in the semiconductor sector.

Program Highlights:

Ms. Pragati Thakur is a Senior HR Manager at Cadence, Hyderabad, with extensive experience in talent acquisition and workforce development in the semiconductor domain. Her insights helped students understand industry expectations and career pathways.

A total of 95 students from B.Tech ECE III and IV Year had attended the program. The session covered global and Indian semiconductor trends, government initiatives, emerging job roles, required technical skills, and career planning strategies. Real-time industry examples were shared.

The IIIC team led by Dr. G.Janardhan Raju, Dean, SoE & Convener, IIIC and Head of ECE department Dr.B.Ravi, along with Dr. B.Hariprasad, Professor, ECE, Mr.Prashanth Kulkarni, Assoc. Prof., ECE, Mr.Koustubh Kulkarni, Asst.Prof, ECE & Mr.K.Srinivas, Asst.Prof, ECE were also the part of program, accompanied by nearly 95 students from B.Tech ECE IV year.

What they do?

Ideabytes company provides efficient IoT Hardware/Software, Embedded Hardware/Software and other AI related services to almost all businesses, pharma, defence organizations as part of digital transformation.



Participants listening to key insights



Team with the session's active participants

Outcome of Program:

A total of 95 students had attended the program. Students actively participated by raising queries, seeking suggestions, and engaging in discussions to gain deeper insights into the semiconductor industry. Students gained clarity on semiconductor career opportunities, industry expectations, and skill development paths. The program strengthened industry–institute interaction. The Industry Expert Talk was informative and impactful, contributing to enhanced student awareness and motivation towards semiconductor-related careers.

4.FACULTY ACHIEVEMENTS

4.1 R & I ACHIEVEMENTS (MAR-APR)

S. No.	Journals			Conferences		Patents		Books/Book Chapters	Total
	SCI-E/SCI	ESCI/WOS/SCOPUS	Others	IEEE	Scopus	Published	Granted		
1.	1	0	0	2	0	3	0	0	6

Journals:


1. Preetam Raj, P. M., **Vittapu, S. K.**, Srinivasan, M. P., Gupta, M., & Gajbhiye, P. (2026) "Prompt Engineering-Based Memristive In-Memory Decimal Calculator." IETE Journal of Research, pp. 1-11. (SCI-E)

Conferences:

1. **K.Anuradha,V.Sherlin Solomi,S.Ravi Chand** "Efficient Fine-Tuning of Large Language Models for Telugu Sentiment Analysis using QLoRA." International Conference on Machine Learning and Autonomous Systems (ICMLAS-2026), Stamford International University Bangkok,Thailand March(pp.1-6). **IEEE (Published).**
2. **Dr. Nitesh Gaikwad** "Deep Hybrid Vision System for Automated Clinical Grading of Diabetic Retinopathy" 4th IEEE International Conference on Interdisciplinary Approaches in Technology and Management for Social Innovation (IEEE IATMSI-2026), IEEE MP Section and ABV-IITM Gwalior March (pp.1-6) **IEEE (Published).**

Patents:

1. **P. Chandana, S. Vivek Sai, P. Shiva Sai, P. mulya, A. Shruthi, R. Prasanna,K. Hemalatha, Soumya G, K. Thrisandhya, P. Kiranmayee** "Optimized design of dual mode hybrid Adder"(India, Patent No. 202641032259A ,The Patent Office Journal No. 13/2026) **(Published).**
2. **Mrs. Shiva Prasanna Kannagulla Mr. Koustubh Kulkarni Ms. M H Sushma Mercilin Mr. Rambabu Kambhampati Ms. Deevi Bindu Tushara Devara Yaswanth Boini Madesh Vattela Vaishnavi Kurma Pranay Banoth Santhosh** "AI FIRMWARE BASED HOME AUTOMATION USING ESP-32 OTFS" (India, Patent No. 202641032736 A, The Patent Office Journal No. 13/2026) **(Published).**

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3. **B. Manisha, C. Sampath, K. Sagar, E. Srinidhi, A. Sindhu, D. Ruthvik, Dr. Ravi Bolimera, Dr. Nitesh Gaikwad, Mr. Kandukuri Srinivas, Mrs. Shetty Sravanthi** “BIDIRECTIONAL VISITOR COUNTER WITH AUTOMATIC ROOM LIGHT CONTROLLER” (India, Patent No.202641032260 A, The Patent Office Journal No. 13/2026) (**Published**).

4.2 FACULTY DEVELOPMENT PROGRAMS

Academic Year: 2025-26	Semester: II	Date: 30/ 04/2026
MARCH 2026- APRIL 2026 Achievements–Faculty Participation		

S. No	Publications	<= 2 Days	2 to 5 Days	> 5 Days	Total
1.	FDP	-	-	4	04

Faculty Development Programs:

S.NO	NAME OF THE FACULTY	TITLE	PLACE/ORGANIZATION	DATE(S)
1	Dr. NITESH GAIKWAD	NLP APPLICATIONS	SREE RAMA ENGINEERING COLLEGE IN ASSOCIATION WITH PANTECH SOLUTIONS PVT.LTD	10-03-2026 TO 14-03-2026
2	Dr. P.ASHOK KUMAR	NLP APPLICATIONS	SREE RAMA ENGINEERING COLLEGE IN ASSOCIATION WITH PANTECH SOLUTIONS PVT.LTD	10-03-2026 TO 14-03-2026
3	Dr. A. GOPAL	DIGITAL VLSI DESIGN USING INDUSTRY STANDARD EDA TOOLS	VEL TECH MULTI TECH Dr. RANGARAJAN Dr. SANKUNTHALA ENGINEERING COLLEGE, AVADI, CHENNAI - 600062.	09-03-2026 TO 14-03-2026
4	Ms. SUSHMA MERCILIN	ANTENNA DESIGN FOR 5G & 6G	MEERUT INSTITUTE OF ENGINEERING AND TECHNOLOGY(AICTE)	30-03-2026 TO 04-04-2026

5. TRAINING & PLACEMENT

5.1 VLSI JOB OPPORTUNITIES - CAREER AWARENESS PROGRAM

Resource Person:

Mr. Nagarjuna Gattu Technical Director, Vikarna Technologies

Participants:

IV Year ECE Students – 54 Students

Time: 11:00 AM to 12:00 PM

Venue: S-22

Faculty Attended:

Dr. Ravi Bolimera, HoD – ECE

Dr. B. Hariprasad Naik, Associate Professor

Ms.N.Lavanya ,Asst.prof



Objective of the Session:

The objective of the session was to create awareness among IV year ECE students about career opportunities in the VLSI domain and to provide insights into industry expectations and required skill sets.

Description of the Session:

The Department of Electronics and Communication Engineering organized a VLSI Job opportunities Awareness Session for IV year students. Mr. Nagarjuna Gattu delivered an informative lecture covering the overview of the VLSI industry, various job roles, essential technical skills, and career pathways in the semiconductor domain. The session was interactive, and students actively participated by raising queries related to skill development, internships, and placement opportunities in VLSI companies.

Outcome of the Session:

- Students gained awareness about VLSI career opportunities
- Understanding of industry-relevant skills was enhanced
- Students were motivated to plan their career roadmap effectively

Conclusion:

The session was highly beneficial and informative. The Department expresses sincere gratitude to the resource person for sharing valuable industry insights and motivating students toward VLSI careers.

6. EDITORIAL COLUMN

6.1 FACULTY EDITORIAL COLUMN

AI-DRIVEN THREAT DETECTION

-By
Mrs. K.Thrisandya
Assistant professor
ECE Department

Introduction

Artificial Intelligence is rapidly redefining how cyber threats are detected and managed. In 2026, AI-driven threat detection has become both a powerful defense tool and a growing concern, as the same technology is now being used by attackers. AI-driven threat detection involves training algorithms to recognize patterns in network traffic, user behavior, and system activity. Once trained, these systems can detect unusual or suspicious behavior that may indicate a cyberattack.

Rising Concerns Over AI-Driven Threats

While AI strengthens security, it also introduces new risks. Powerful AI models can now:

- Discover software vulnerabilities automatically
- Simulate complex cyberattacks
- Generate exploit code within hours

Experts warn that such capabilities could lead to **large-scale cyber incidents** if misused. Financial institutions and governments are increasingly cautious, as advanced AI tools have the potential to disrupt critical systems and expose sensitive data.

Real-World Cyberattacks Using AI

Recent cyber incidents highlight how attackers are leveraging AI. In one major breach, a single hacker used AI tools to target multiple government agencies and steal massive amounts of data. The attack demonstrated how AI can:

- Automate hacking processes
- Reduce attack time significantly
- Perform tasks that previously required large teams

This marks a shift toward AI-powered cybercrime, making attacks faster and more sophisticated.

Industry Response and Adoption

Organizations across industries are adopting AI-driven threat detection systems to strengthen their defenses. Banks and enterprises are testing advanced AI tools that can identify thousands of vulnerabilities quickly, improving long-term cybersecurity resilience. At the same time, cybersecurity companies are moving toward AI-native security solutions, focusing on automation, predictive analytics, and real-time monitoring.



6.2 STUDENT EDITORIAL COLUMN

CYBER SECURITY: PROTECTING THE DIGITAL WORLD IN 2026

-By
N.ROHITH
III-ECE-B
(237Z1A04D2)

In today's interconnected world, cyber security has become one of the most critical aspects of technology. With the rapid growth of the internet, cloud computing, and smart devices, protecting digital systems from cyber threats is more important than ever.

What is Cyber Security?

Cyber security refers to the practice of protecting computers, networks, and data from unauthorized access, attacks, or damage. It involves a combination of technologies, processes, and strategies designed to safeguard sensitive information.

Why Cyber Security is Important

As individuals and organizations rely heavily on digital platforms, cyber risks continue to grow. A single cyberattack can lead to:

- Loss of sensitive data
- Financial damage
- Identity theft
- Disruption of services

This makes cyber security essential for businesses, governments, and everyday users.

Common Cyber Threats

Some of the most common cyber threats include:

- Phishing Attacks: Fraudulent messages that trick users into revealing personal information
- Ransomware: Malware that locks data and demands payment
- Malware: Harmful software designed to damage or disrupt systems
- DDoS Attacks: Flooding a network to make it unavailable
- Insider Threats: Risks from individuals within an organization

Understanding these threats helps in preventing them effectively.

Key Cyber Security Measures

To stay protected, the following practices are essential:

- Use strong and unique passwords
- Enable multi-factor authentication (MFA)
- Keep software and systems regularly updated
- Install antivirus and firewall protection
- Avoid clicking on suspicious links or emails

These simple steps can significantly reduce the risk of cyberattacks.

Role of Artificial Intelligence

Artificial Intelligence is playing a major role in modern cyber security. AI systems can:

- Detect unusual patterns in network activity
- Identify potential threats quickly
- Respond to attacks in real time

This makes cyber defense faster and more efficient.

Future of Cyber Security

The future of cyber security will focus on:

- AI-driven threat detection
- Stronger encryption methods
- Protection against quantum computing threats
- Increased awareness and training

As technology evolves, cyber security strategies must also adapt to new challenges.

Conclusion

Cyber security is essential for ensuring a safe digital environment. With increasing cyber threats, it is important for everyone to adopt good security practices and stay informed about emerging risks. Protecting data today means securing the future.

7. ALMA CORNER

7.1 ALMA CONNECT

The Department of Electronics & Communication Engineering, School of Engineering, Nalla Narasimha Reddy Education Society's Group of Institutions, organized an Alumni Interaction Program titled "Alma Connect" on 4th March 2026 at the ECE seminar hall.

Speaker: Mr. Shivaprasad Sevarthi (217Z5A0421)

Title of the Program: Alma Connect – Senior–Junior Interaction

Date: 04.03.2026

Time: 12:00 Noon

Venue: ECE Seminar Hall

Resource Person:

Mr. Shivaprasad Sevarthi (217Z5A0421)

(2021–2025 Batch Alumni)

Physical Design Trainee

Sandlogic Technologies Pvt Ltd

Target Audience: ECE Students (III Year)

Objectives of the Program:

- To strengthen alumni–student interaction
- To provide guidance on career opportunities in the semiconductor and VLSI industry
- To motivate students towards skill development and industry readiness



Brief Description of the Program:

The Department of Electronics & Communication Engineering organized “Alma Connect – Senior–Junior Interaction” on 04th March 2026 as part of alumni engagement and student development initiatives. The session was delivered by Mr. Shivaprasad Sevarthi, an alumnus of the department and currently working as a Physical Design Trainee at Sandlogic Technologies Pvt Ltd. During the session, he shared his academic journey, preparation strategies, and experiences in entering the semiconductor industry.

He explained the importance of VLSI design, physical design concepts. The session was interactive with students asking questions related to career paths, internships, interview preparation, and skill development, certifications, and continuous learning.

Conclusion:

The Alma Connect – Senior–Junior Interaction program was successfully conducted and proved to be highly informative and motivating for students. The department looks forward to organizing more such alumni interaction sessions in the future.

7.2 ALMA REFLECTION



I am G.Sai Vardhan 217Z1A0429, a dedicated and motivated individual with a strong passion for continuous learning, personal growth, and professional excellence. Throughout my academic and professional journey, I have developed valuable skills in problem-solving, communication, teamwork, leadership, and analytical thinking, enabling me to contribute effectively to organizational success and community development.

During my educational and practical experiences, I have successfully applied theoretical knowledge to real-world situations, enhancing my ability to analyze challenges, develop innovative solutions, and achieve desired outcomes. By utilizing tools, technologies, and industry best practices, I have improved efficiency, strengthened collaboration, and delivered quality results within stipulated timelines. These experiences have helped me build confidence, adaptability, and a commitment to lifelong learning.

In addition to my academic and professional pursuits, I have actively participated in leadership and extracurricular activities that have strengthened my interpersonal, organizational, and decision-making skills. Working with diverse teams has taught me the importance of collaboration, accountability, and effective stakeholder engagement. These experiences have enabled me to contribute meaningfully to projects and initiatives while fostering a positive and inclusive environment.

